AMENDMENTS TO THE SPECIFICATION

Pages 4-5

Please replace the paragraph commencing at line 23 of page 4 with the following amended paragraph:

--Fig. 1 is a system configuration of a wireless LAN system according to a first embodiment of the present invention; Fig. 2 is a block diagram of an internal configuration of an STA used in the system according to the first embodiment; Fig. 2A is a block diagram illustrating an exemplary embodiment in which elements of the STA in Fig. 2 are arranged according to an uplink packet processor and a downlink packet processor; Fig. 3 is a block diagram of an internal configuration of a diversity device used in the system according to the first embodiment; Fig. 3A is a block diagram illustrating an exemplary embodiment in which elements of the diversity device in Fig. 3 are arranged according to an uplink packet processor and a downlink packet processor; Fig. 4 depict the configuration of a packet with a sequence number added thereto; Fig. 5 depicts the configuration of a packet with a sequence number and received radio wave status information added thereto; Fig. 6 is a system configuration of a wireless LAN system according to a sixth embodiment of the present invention; Fig. 7 is a block diagram of an internal configuration of a layer 2 switch used in the system according to the sixth embodiment; and Fig. 8 is a block diagram of an internal configuration of an STA used in the system according to the sixth embodiment.--

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Immediately before the paragraph commencing at line 7, please insert the following new paragraph:

--According to an exemplary embodiment, the various operations for processing the uplink packets which are performed by the diversity registration unit 220, the SN adding unit 160, the copy generating unit 170, and tunnel generating unit 180 may be implemented within an uplink packet processor 250 as shown in Fig. 2A. However, such operations may be

implemented according to other arrangements of hardware, software, and/or firmware as will be contemplated by those of ordinary skill in the art.--

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Immediately before the paragraph commencing at line 2, please insert the following new paragraph:

--According to an exemplary embodiment, the various operations for processing the downlink packets which are performed by the tunnel cancelling unit 190, the selection processor 200, and the SN detecting unit 210 may be implemented within a downlink packet processor 260 as shown in Fig. 2A. However, such operations may be implemented according to other arrangements of hardware, software, and/or firmware as will be contemplated by those of ordinary skill in the art.--

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Immediately before the paragraph commencing at line 29, please insert the following new paragraph:

--According to an exemplary embodiment, the various operations for processing the downlink packet which are performed by the SN adding unit 360, the copy generating unit 370, and the tunnel generating unit 410 of the diversity device 120 may be implemented within a downlink packet processor 430 as shown in Fig. 3A. However, such operations may be implemented according to other arrangements of hardware, software, and/or firmware as will be contemplated by those of ordinary skill in the art.--

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Immediately before the paragraph commencing at line 24, please insert the following new paragraph:

--According to an exemplary embodiment, the various operations for processing the uplink packet which are performed by the tunnel cancelling unit 390, the selection processor 400, and the SN deleting unit 410 may be implemented within an uplink packet processor 440 as

shown in Fig. 3A. However, such operations may be implemented according to other arrangements of hardware, software, and/or firmware as will be contemplated by those of ordinary skill in the art.--